

Electronic Patent Application Fee Transmittal

Application Number:	10722193
Filing Date:	26-Nov-2003
Title of Invention:	INTEGRATED CIRCUIT DEVICES HAVING BURIED INSULATION LAYERS AND METHODS OF FORMING THE SAME
First Named Inventor:	Byeong-chan Lee
Filer:	Robert N. Crouse/Candi Riggs
Attorney Docket Number:	5649-1225

Filed as Large Entity

Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Post-Allowance-and-Post-Issuance:

Utility Appl Issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700